LISTING OF CLAIMS

1. **(Currently Amended)** An endoscope apparatus, comprising:

a first drive signal generator portion for generating a first drive signal from a signal source for driving an imaging device built in or removably connected to said endoscope;

a video signal extracting portion for obtaining a first video signal included in ~~a~~ an imaging signal obtained in said imaging device;

a second drive signal generator portion for generating a second drive signal for controlling a timing when said video signal extracting portion obtains said first video signal from said imaging signal;

a first processor that includes as part of said first processor at least a part of a circuit for obtaining, from said first video signal, a second video signal that can be displayed on a monitor; and

a delay circuit, which is included as part of said first processor and at least partially interposed between said signal source and said first drive signal generator, for delaying at least part of signals among signals included in said first drive signals and said second drive signals.

2. **(Previously Presented)** An endoscope apparatus according to claim 1, wherein said first processor is a digital signal processor constructed as an integrated circuit.

3. **(Original)** An endoscope apparatus according to claim 1, wherein said delay circuit is variable in its delay time.

4. **(Original)** An endoscope apparatus according to claim 3, wherein said delay circuit comprises a multistage buffer circuit connected in series and a circuit for selecting the number of stages of said multistage buffer circuit.

5. **(Original)** An endoscope apparatus according to claim 3, comprising a second processor for setting the delay time of said delay circuit.

6. **(Original)** An endoscope apparatus according to claim 5, comprising:

a switch for specifying said delay time; and

said second processor setting said delay time depending on the condition of said switch.

7. **(Original)** An endoscope apparatus according to claim 5, comprising:

a switch for setting information from which said delay time can be derived; and

said second processor setting said delay time depending on the condition of said switch.

8. **(Original)** An endoscope apparatus according to claim 7:

wherein information from which said delay time can be derived includes information indicating length of an insert portion of said endoscope.

9. **(Previously Presented)** An endoscope apparatus according to claim 7:

wherein information from which said delay time can be derived includes identification information for identifying a type of said endoscope.

10. **(Previously Presented)** An endoscope apparatus according to claim 5, comprising:

said endoscope including information acknowledgment portion for giving information indicating said delay time to said second processor; and

said second processor setting said delay time depending on information acknowledged from said information acknowledgment portion.

11. **(Previously Presented)** An endoscope apparatus according to claim 5, comprising:

said endoscope including a information acknowledgment portion for giving information from which said delay time can be derived to said second processor; and

said second processor setting said delay time depending on information acknowledged from said information acknowledgment portion.

12. **(Previously Presented)** An endoscope apparatus according to claim 11:

wherein information from which said delay time can be derived includes information indicating length of an insert portion of said endoscope.

13. **(Original)** An endoscope apparatus according to claim 11:

wherein information from which said delay time can be derived includes identification information for identifying a type of said endoscope.